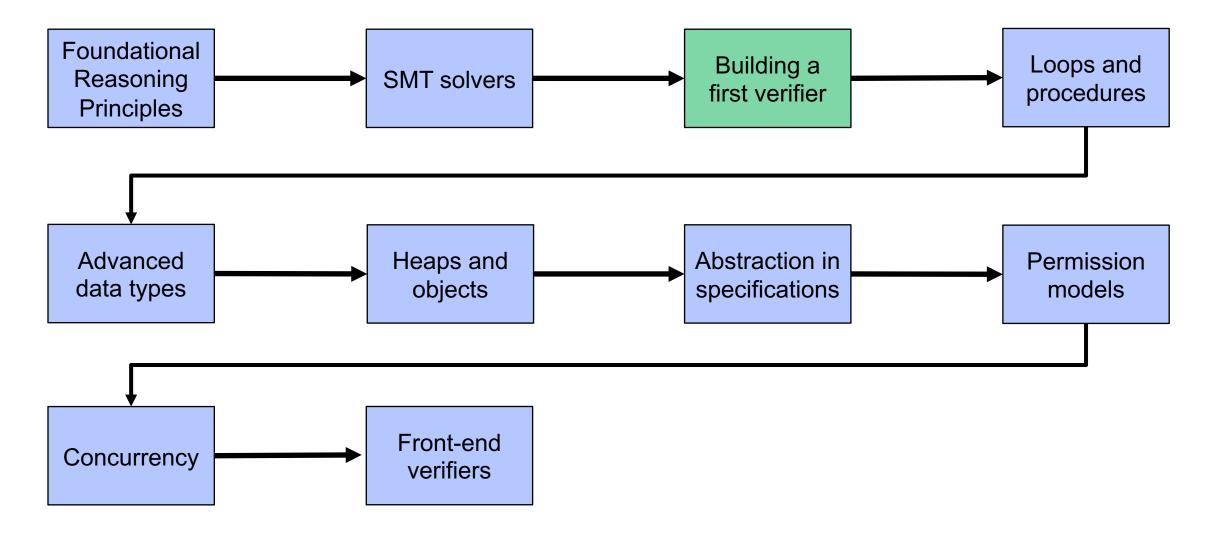
02245 - Lecture 3

BUILDING VERIFIERS

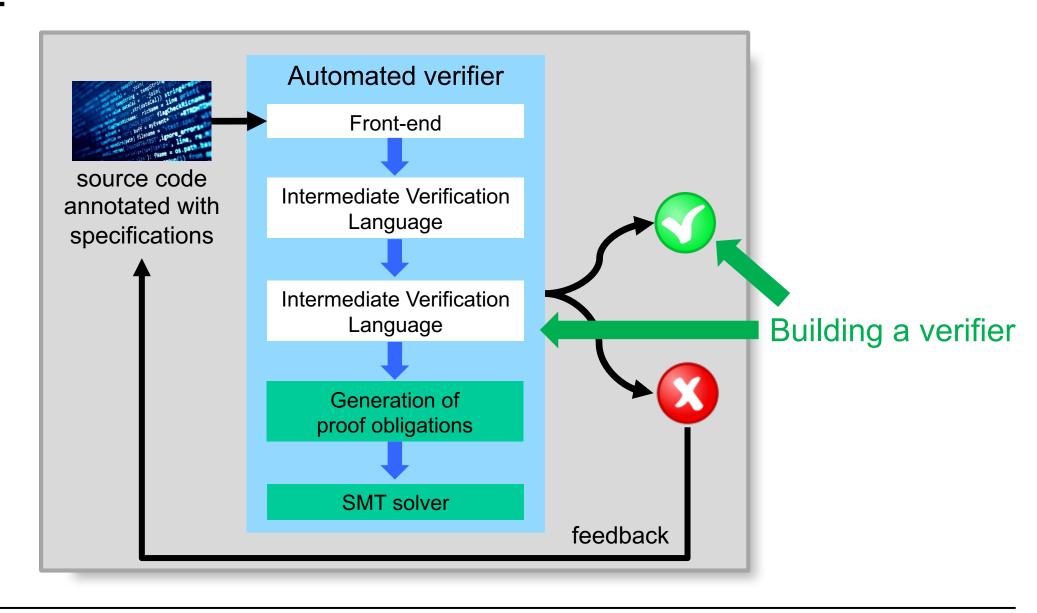


Tentative course outline





What next?

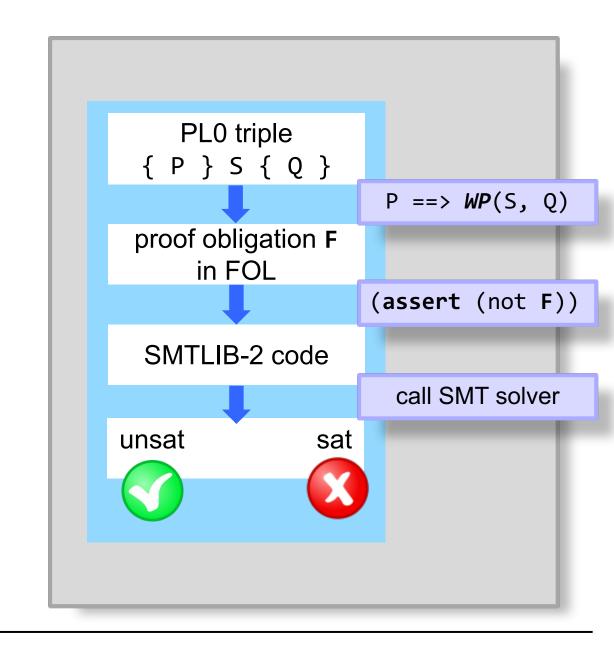


Outline

- 1. The Verification Toolchain
- 2. Efficient weakest preconditions
- 3. Error localization

The toolchain so far

- "Verification as compilation"
- Translate verification problems into simpler ones until the answer is trivial
- Wishlist for each translation A -> B
 - Soundness: If B is valid, then A is valid
 - Completeness: If A is valid, then B is valid
 - Efficiency: B's size is reasonable wrt. A
 - Explainability: We can reconstruct errors in
 A from errors in B



Splitting the PL0 Language

Programming Language XPL

- Statements are eXecutable
- Deterministic conditionals
- Specifications via triples


```
Verification condition
{ P } S { Q } valid
```

Verification Language PL0

- Statements model verification problems
- Nondeterministic choice
- Verification-specific statements

What is our verification condition for PL0 programs if we have only a statement S (no pre- or postcondition)?

Exercise: From XPL triples to PL0 statements

Define an encoding **ENC** that takes an XPL triple

```
{ P } S { Q }
```

and yields a PL0 statement such that your encoding is

- 1. sound,
- 2. complete,
- 3. efficient, and
- 4. explainable

with respect to the verification conditions of XPL and PL0.

Justify why (1) - (4) holds for your encoding.

Try to give formal statements. Proofs are not required.

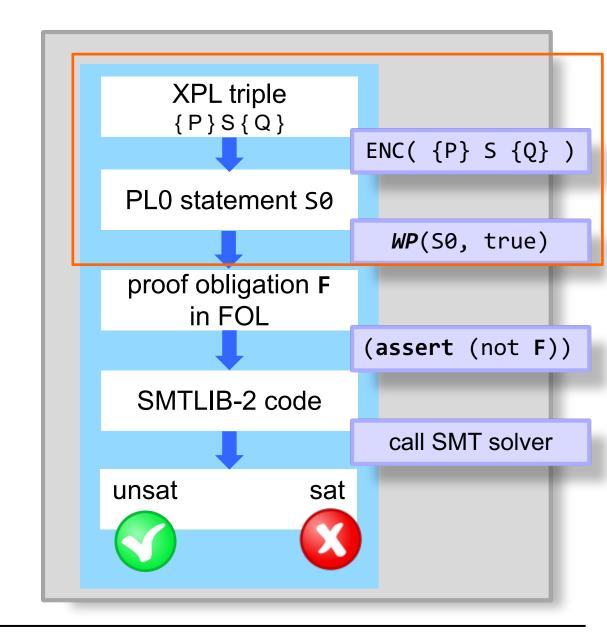
Running example: triple_min

```
method triple_min(x: Int, y: Int) returns (z: Int)
requires x >= 0 && y >= 0
z := x - y
  if (z < 0) {
     z := z + y
     z := z + 2 * x
  } else {
    z := z - x
    z := z + 4 * y
```

The code examples contain every translation step applied to this program

The toolchain so far

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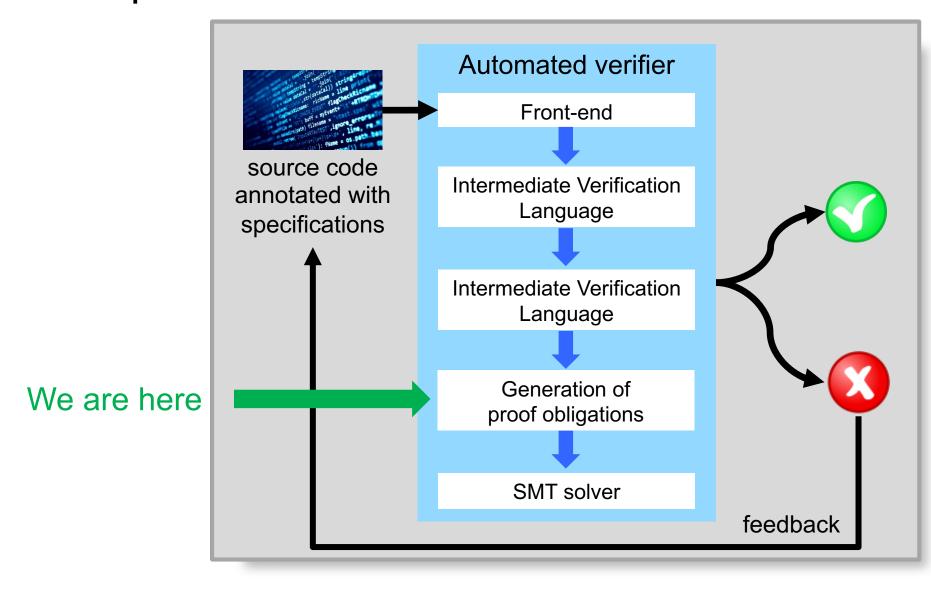
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Roadmap



Verifier Performance

- The time consumed by an automated verifier is typically dominated by the SMT solver
- Factors influencing SMT performance
 - Size of verification conditions
 - Theories in the background predicate
 - Effectiveness of heuristics for undecidable theories, particularly quantifier instantiation
- Verification times are flaky
 - Minor changes in VCs can have major impact
 - Verification is often much faster than refutation

```
performance.vpr U X
                                                                                          th II ··
                  define INT MIN (-2147483648)
                   define INT MAX (2147483647)
                  method main()
                        var i: Int
                        var res: Int
R
                        assume INT MIN <= i && i <= INT MAX
                        if (i < 0) {
           13
                              assert INT MIN <= res && res <= INT MAX
                        } else {
                              res := i
 $\mathbb{P} main* \cdot \infty \omega 0 \textit{\Omega} 0 \quad \text{carb} \text{ or Verification of performance.vpr 99% \text{ \text{TF-8} CRLF Viper \overline{\mathcal{P}} \text{ \text{$\text{V}}}
```

Size of Verification Conditions

Compute WP(S, Q) for the programs below; do you notice a pattern?

```
{ TODO }
res := (start + end)/2
{ res * res * res == x }
```

```
{ TODO }
{
    x := (y+z)*(y+z)
} [ ] {
    x := 12
}
{ 0 <= x }</pre>
```

Eliminating duplication from assignments

Idea: add knowledge x == a once and for all instead of substituting every x by a

```
WP(x := a, Q) ::= (x == a) ==> Q
```

Example with current WP

```
{ <u>(start + end)/2</u> * <u>(start + end)/2</u> * 
 <u>(start + end)/2</u> == x } 
res := (start + end)/2
{ res * res * res == x }
```

Example with proposed WP

```
{ res == (start + end)/2 ==>
  res * res * res == x }

res := (start + end)/2
{ res * res * res == x }
```

Is the proposed change of *WP* sound?

Eliminating redundancy from choice-statements

Similar idea: factor out postcondition using a fresh variable

```
WP(S1 [] S2, Q) ::= (B == Q) ==> WP(S1, B) && WP(S2, B) where B is a fresh Boolean variable
```

Soundness of alternative rule for choices

```
WP(S1 [] S2, Q) ::= (B == Q) ==> WP(S1, B) && WP(S2, B) where B is a fresh Boolean variable
```

Is the proposed change of WP sound?

Towards efficient verification conditions

Choices: sound and efficient rule for programs without assignments

$$WP(S1 [] S2, Q) ::= (B == Q) ==> WP(S1, B) && WP(S2, B)$$
 where B is fresh

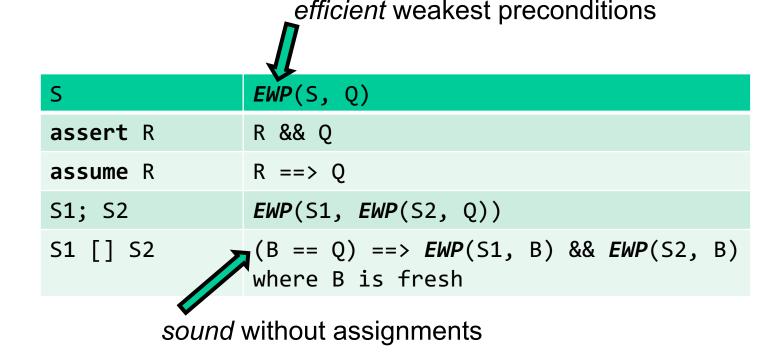
Assignments: sound and efficient rule

$$WP(x := a, Q) ::= (y == a) ==> Q[x / y]$$
 where y is fresh

■ **Observation:** if x is not free in a or Q, then

→ Can we translate PL0 into a reduced verification language without assignments?

The minimal verification language MVL



- PL0: WP(S, Q) is exponential in the size of S and Q
- MVL: *EWP*(S, Q) is linear in the size of S and Q
- → Is there a sound & complete encoding from PL0 to MVL?

From PL0 to MVL

- Main idea:
 - 1. Eliminate variable declarations (exercise, later)
 - 2. Make all assignments assign to fresh variables → single static assignment form (SSA)
 - 3. Replace every assignment x := a by assume $x == a \rightarrow passification$
- Observation: all paths through a PL0 program are finite (no loops / recursion)
- A program is in dynamic single assignment form (DSA)

iff every assignment on a path assigns to a fresh variable

```
x := 0

x := 1

y := x

x1 := 0

x2 := 1

y1 := x2
```

```
x := 0
{
    x := (y+z)*(y+z)
} [] {
    x := -12
}
x1 := 0
{
    x2 := (y1+z1)*(y1+z1)
} [] {
    x2 := -12
}
```

DSA Construction

- Main idea
 - Introduce multiple versions of each variable
 - Always use the latest version
- Assignment
 - Assign to a new version
- Choice-statements
 - convert both branches individually
 - synchronize the last version of each variable

```
x := 0

x := 1

y := x
x_1 := 0

x_2 := 1

y_1 := x_2
```

```
X := 0
{
    x := (y+z)*(y+z)
    x := 7
} [] {
    x := -12
    x<sub>1</sub> := 0
{
        x<sub>2</sub> := (y<sub>1</sub>+z<sub>1</sub>)*(y<sub>1</sub>+z<sub>1</sub>)
        x<sub>3</sub> := 7
} [] {
        x<sub>2</sub> := -12
        x<sub>3</sub> := x<sub>2</sub>
}
y := x
```

How do we encode variable declarations in MVL?

Hint: try to encode var x as a PL0 program first

S	WP (S, Q)
var x	<pre>forall x :: Q</pre>
x := a	Q[x / a]
assert R	R && Q
assume R	R ==> Q
S1; S2	WP(S1, WP(S2, Q))
S1 [] S2	WP(S1, Q) && WP(S2, Q)

The toolchain so far

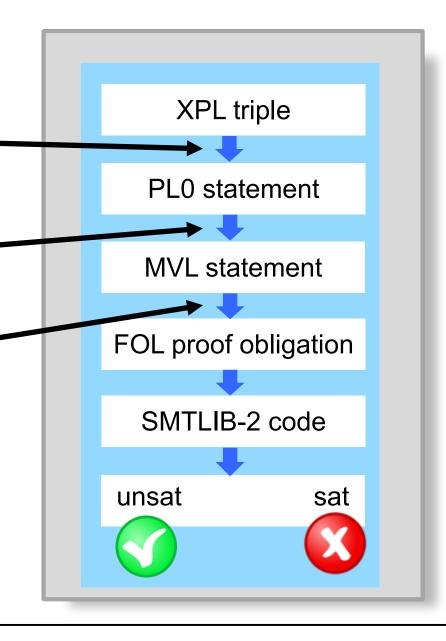
Encode

- Pre- and postconditions
- If-statements
- Variable declarations
- DSA transformation
- Passification

All encodings are sound and complete

(not necessarily true for solvers)

Size of VCs: linear in the original triple





Efficient WP

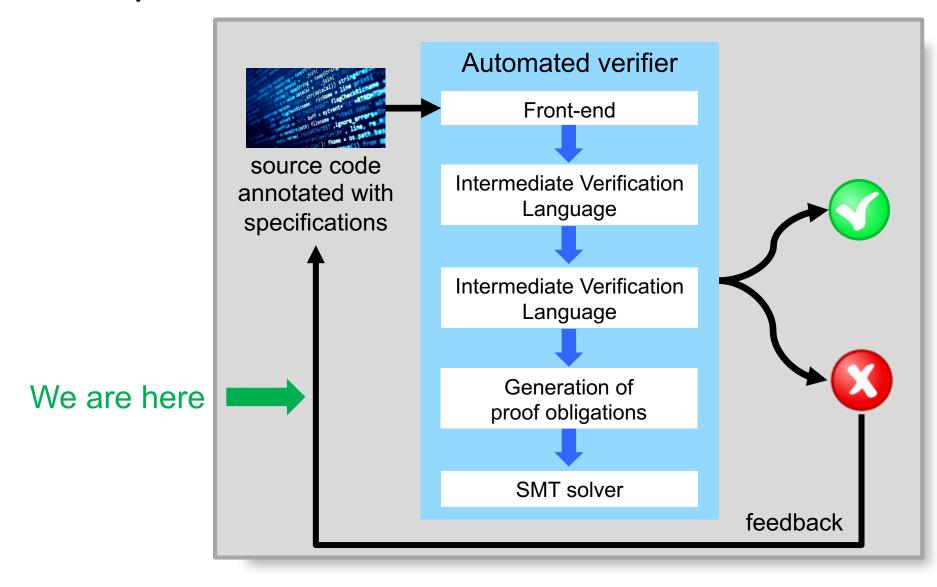
Outline

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Roadmap



Verification Debugging with Counterexamples

Verification condition: !(E)WP(S, true) satisfiable?

unsat:



sat:



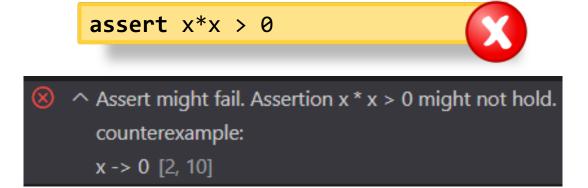
+ model with initial values invalidating VC → counterexample

unknown:



+ we can often still get a partial model

- Viper command line option
 - --counterexample variables



Causes for verification failures

- Errors in the implementation
- Errors in the specification
 - Pre- and postconditions
 - Assumptions and assertions
- Incompleteness of the verifier
- Unsoundness of the SMT solver
 - Possible but unlikely for unverified solvers

```
{ 0 \le b*b - 4*c }
discriminant := b*b - 4*a*c;
x := (-b + \sqrt{discriminant}) / 2
{ a*x^2 + b*x + c = 0 }
```

→ Verifiers should help users to localize and fix verification failures

How does verification fail?

Verification condition: (E)WP(S, true) valid



If S contains no assertions, then (E)WP(S, true) is valid.

How many assertions could fail? Which ones should we report?

```
\{ (x < 17 ==> x < 26) \}
 && (x >= 17 ==> x > 42 & x > 17 & x != 16)
 \{ x < 17 ==> x < 26 \}
 assume x < 17;
 \{ x < 26 \}
 assert x < 26
 { true }
 assume x >= 17;
 \{ x > 42 \&\& x > 17 \&\& x != 16 \}
 assert x > 42;
 { x > 17 && x != 16 }
 assert x > 17;
 { x != 16 }
 assert x != 16
 { true }
} { true }
```

Error localization

If S contains no assertions, then (E)WP(S, true) is valid.

- Goal: report assertions that fail verification
- How to identify failing assertions?
- How many failing assertions should we report?
- How do we deal with dependencies between failures?

```
assert MIN_INT <= x + y
assert x + y <= MAX_INT
res := x + y

assert MIN_INT <= x - y
assert x - y <= MAX_INT
d := x - y

assert d != 0
res := res / d</pre>
```

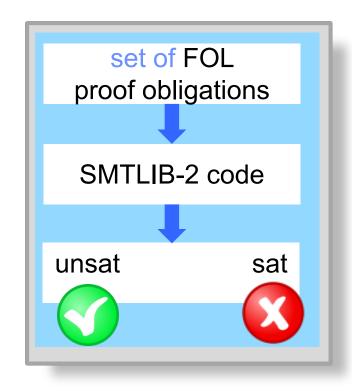
→ A single VC *EWP*(S, true) cannot report which parts of a proof fail

Idea: Split VC at assertions into *multiple* proof obligations

	Sets of predicates
S	MWP(S, M)
assert R	M U {R}
assume P	$\{ P ==> Q \mid Q \in M \}$
S1; S2	MWP(S1, MWP(S2, M))
S1 [] S2	$MWP(S1, M) \cup MWP(S2, M)$

- New verification condition:
 Every P in MWP(S, {}) is valid
- All predicates are implication chains

sate of predicates



Exercise: error localization

- Compute MWP(S, {}) for the statement on the right.
- Which of the proof obligations are valid?
- For each invalid proof obligation, determine an initial state such that the corresponding assertion fails
- Verify the example on the right in Viper using the Carbon verifier. How many error messages do you get?

```
{
   assert x == 7
} [ ] {
   assert x == 2
   assert x > 0
}
```

```
method foo(x: Int, b: Bool) {
   if(b) {
     assert x == 7
   } else {
     assert x == 2
     assert x > 0
   }
}
```

Avoiding masked verification errors

WP and MWP ignore the order of assertions

```
WP(assert P; assert R, Q) = P && R && Q

MWP(assert P; assert R, M) = M U { P } U { R }
```

```
assert x == 2
assert x > 0
```

assert x > 0
assert x == 2

- Issue: second assertion should only be checked if it passed the first assertion
- Solution: add an assumption after each assertion

```
assert R assume R
```

Avoiding masked verification errors

```
{ x == 2 ==> x > 0, x == 2 }
assert x == 2
{ x == 2 ==> x > 0 }
assume x == 2
{ x > 0 }
assert x > 0
{ }
assert x > 0
{ }
assume x > 0
{ }
```





```
{ x > 0 ==> x == 2, x > 0 }
assert x > 0
{ x > 0 ==> x == 2 }
assume x > 0
{ x == 2 }
assert x == 2
{ }
assume x == 2
{ }
```

Case 1: one assertion fails

Case 2: both assertions fails

The toolchain so far

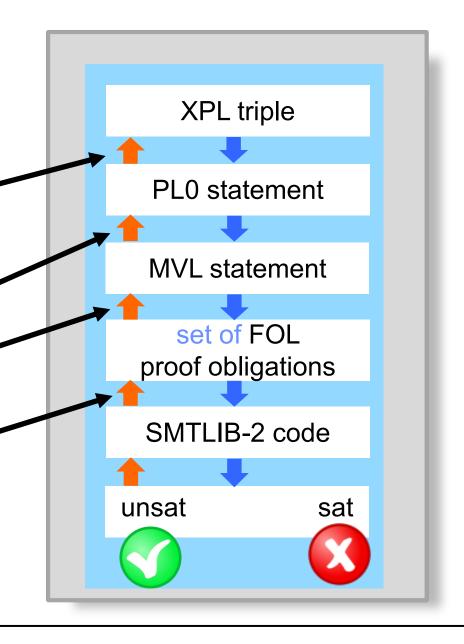
All encodings are sound and complete

Encode XPL triple Pre- and postconditions _____ If-statements PL0 statement Variable declarations **MVL** statement DSA transformation Passification set of FOL Avoid masked errors proof obligations Efficient MWP SMTLIB-2 code unsat sat

The Error Propagation Toolchain

Keep back-translation map from encoding to original → report errors for original problem

- Assertions → postconditions, assertions
- Assume/Choice statements → if-statements
- Versioned variables (DSA) → original variables
- Assumptions → assignments, masked errors
- Proof obligations → assertions
- Solver results → proof obligations



Wrap-up



Error reporting in Viper

- Viper has two verification backends
 - Counterexamples can be enabled via command line option

- Carbon
 - Uses weakest preconditions, similarly to the technique taught in this course, but uses a more efficient approach
 - Reports multiple verification failures

- Silicon
 - Uses symbolic execution (similar to SP)

- Reports one verification error per method
- Default verifier in the IDE

Bonus: more efficient error localization

- Issue with error localization via MWP
 - duplicates theory reasoning
 - cannot use all of **EWP**
 - need extra mapping for back-translation
- Alternative: error localization at SMT level
 - Idea: add a fresh Boolean variable L (label) that is false iff the assertion at position L fails
 - lookup in model which labels are false
- Problem: solver can always set labels to false
 - L=false should only hold if A holds
 - Requires dedicated solver support (e.g. Z3:named)

```
!WP(assert A, Q) sat
iff
   !(A && Q) sat
iff
   !A || !Q sat
iff (L is fresh)
   (!A && !L) || !Q sat
iff
   !WP(assert A || L, Q) sat
```

adding labels is sound

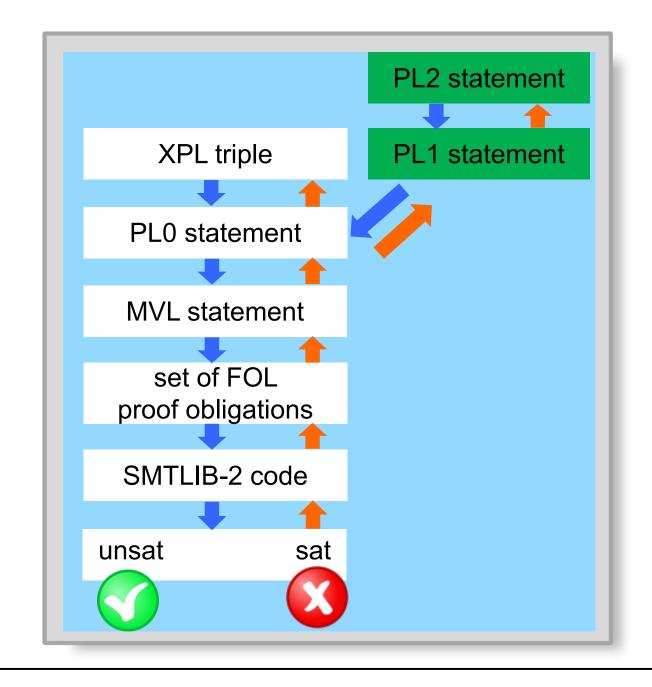
Bonus: more efficient error localization

```
(set-option :produce-assignments true); enables use of named labels
(assert (not
                           ; *not* a negation → term with :attributes
       (= z2 (* 3 x0))
                           ; original assertion
                           ; add Label L6
       :named L6
))
; ...
```

What next?

- More interesting programming and specification constructs
- "Verification as compilation"
- Wishlist for each translation A

 B
 - Sound encodings
 - Complete encodings
 - Linear-size verification conditions
 - Localize and back-translate errors



Tentative course outline

