



# Let this Graph be your Witness!

An Attestor for Verifying Java Pointer Programs

Hannah Arndt, Christina Jansen, Joost-Pieter Katoen,  
Christoph Matheja, Thomas Noll

# Motivation

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**Writing** and **analyzing** pointer programs is challenging:

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Java pointer programs**



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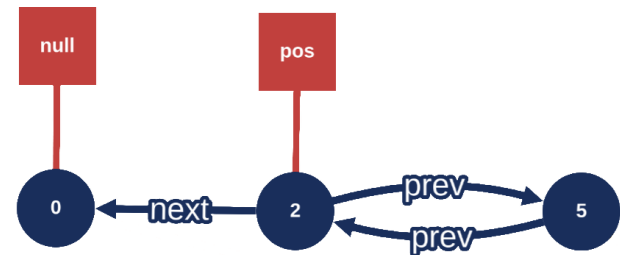
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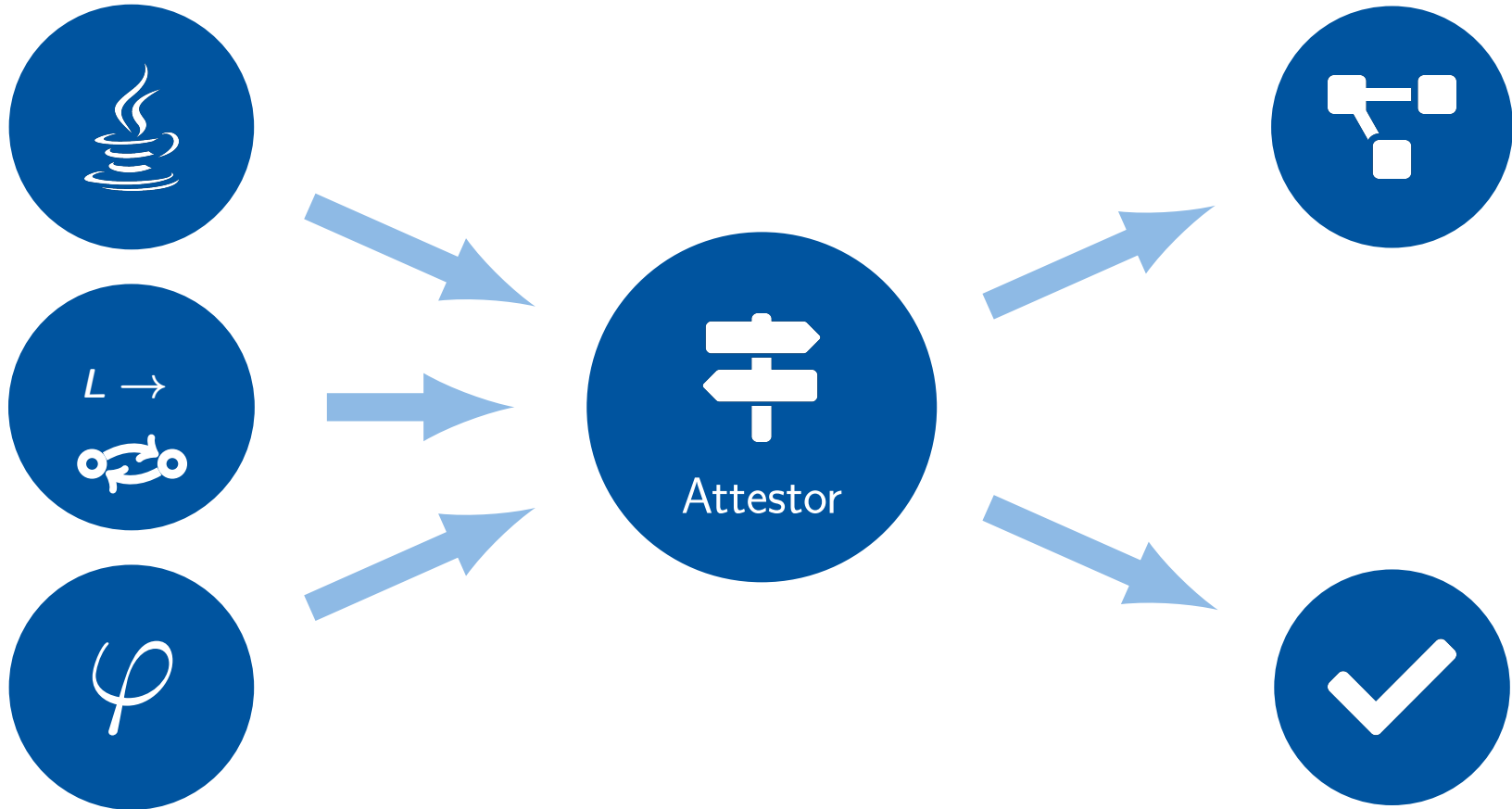
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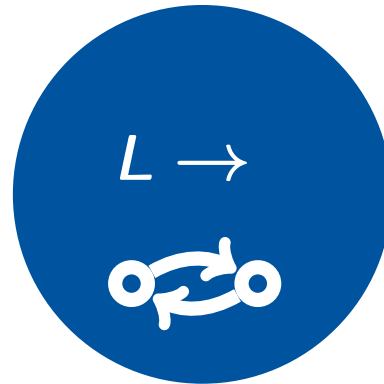
# Input / Output

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# Input: Graph Grammar

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## Program states

program states are

graphs



# Input: Graph Grammar

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## Program states

abstract program states are languages of graphs



# Input: Graph Grammar

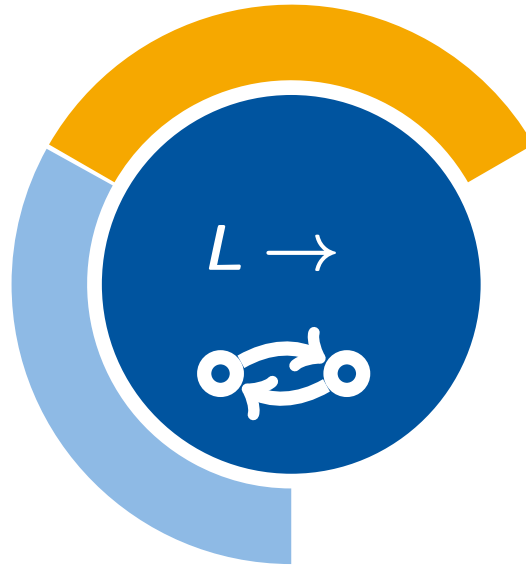
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## Program states

abstract program states are languages of graphs

## Graph Grammars

(indexed) HRG



# Input: Graph Grammar

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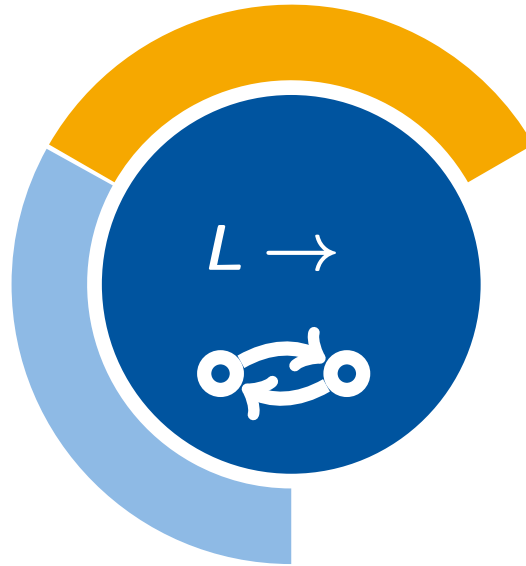
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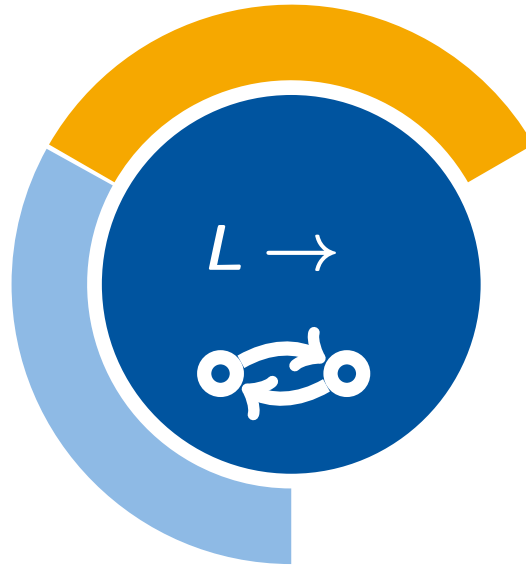
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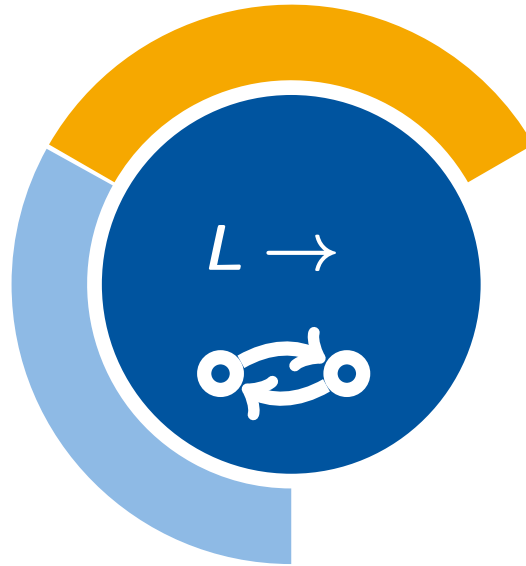
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guide abstraction



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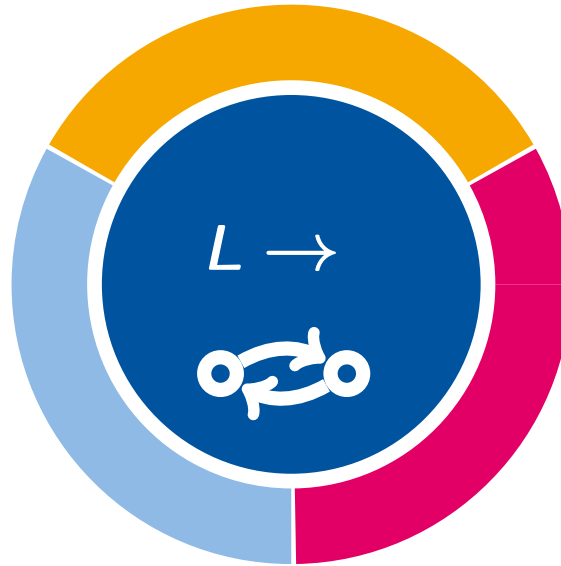
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## Examples

(doubly-)linked lists

lists of lists

skip lists

(balanced) trees

# Input: LTL Specification

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## Specifications

LTL



## Examples

$\mathcal{G} \mathcal{F} \text{ empty}$

# Input: LTL Specification

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## Specifications

LTL

+ points-to relations



## Examples

$\mathcal{G} \mathcal{F} \text{ empty}$

`x.next != null`

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## Specifications

LTL

+ points-to relations

+ reachability



## Examples

$\mathcal{G} \mathcal{F} \text{ empty}$

$x.\text{next} \neq \text{null}$

$x \xrightarrow{\text{next}} y$

# Input: LTL Specification

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## Specifications

LTL

- + points-to relations
- + reachability
- + grammar languages



## Examples

$\mathcal{G} \mathcal{F} \text{ empty}$

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heap tree shaped



# Input: LTL Specification

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## Specifications

LTL

- + points-to relations
- + reachability
- + grammar languages
- + object-tracking



## Examples

$\mathcal{G} \mathcal{F} \text{ empty}$

$x.\text{next} \neq \text{null}$

$x \xrightarrow{\text{next}} y$

heap tree shaped

$\forall e : \mathcal{F}(x == e)$

# Attestor: State Space Generation

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## Materialization

grammar derivations



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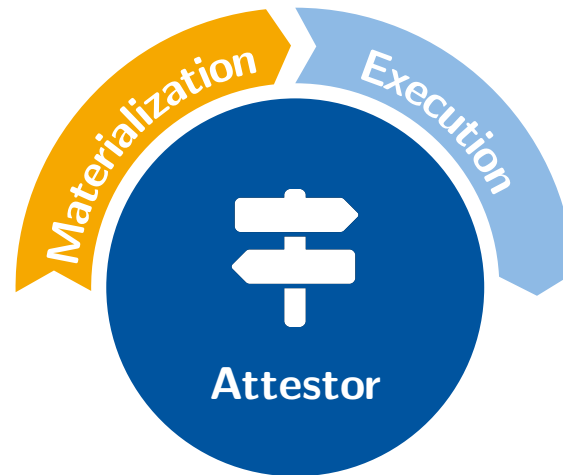
## Materialization

grammar derivations

## Execution

graph transformations

procedure modular

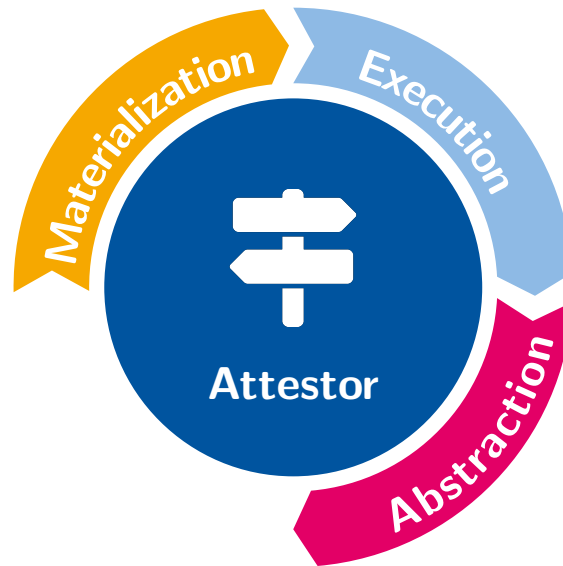


# Attestor: State Space Generation

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## Materialization

grammar derivations



## Execution

graph transformations  
procedure modular

## Abstraction

inverse derivations

# Attestor: State Space Generation

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## Materialization

grammar derivations

## Execution

graph transformations

procedure modular

## Labeling

model checking problem

uses heap automata [ESOP'17]

## Abstraction

inverse derivations

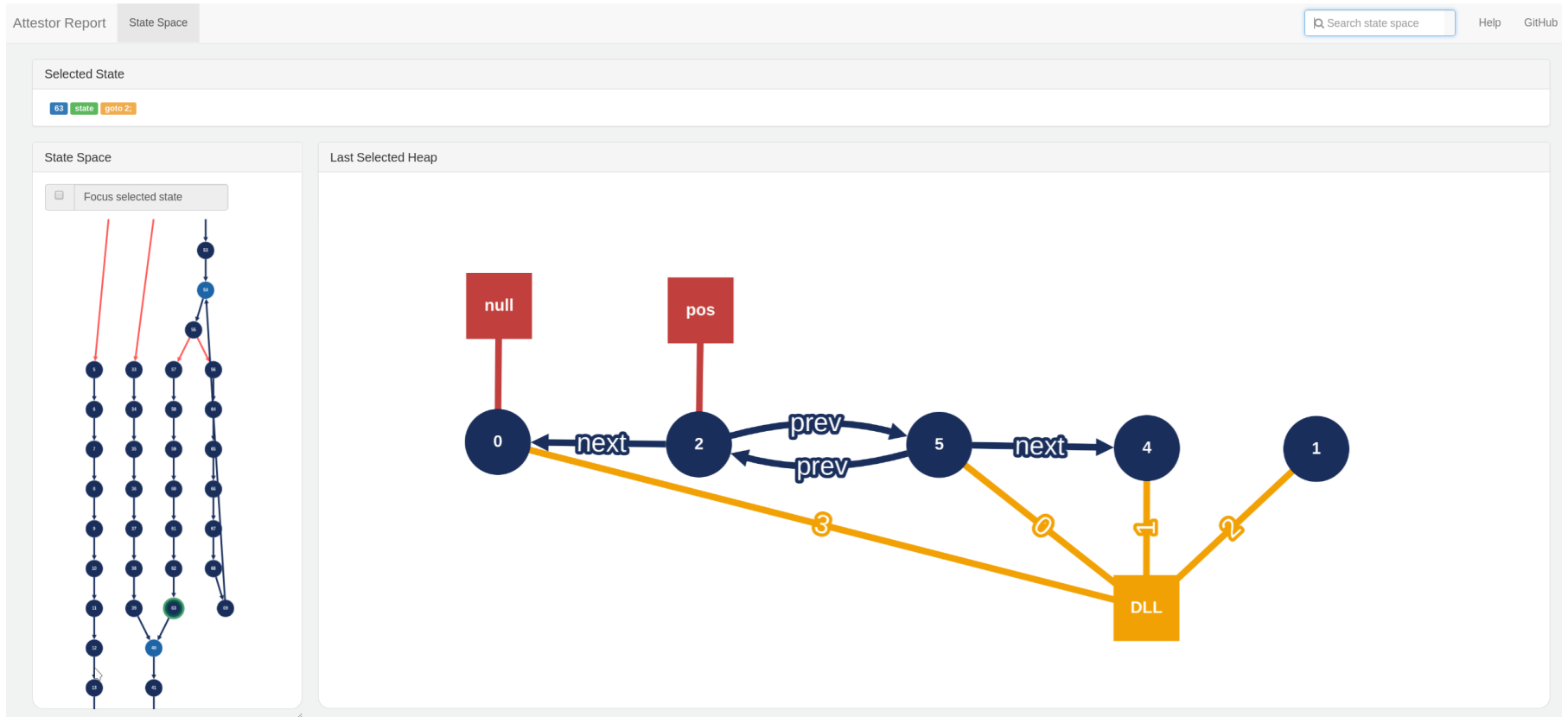


# Output: State Space

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# Output: Specification Satisfied?

---



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## Built-in LTL Model Checker

yes + proof



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## Built-in LTL Model Checker

yes + proof

don't know



# Output: Specification Satisfied?

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## Built-in LTL Model Checker

yes + proof

don't know

no + counterexample



# Output: Specification Satisfied?

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## Counterexamples

possibly spurious

# Output: Specification Satisfied?

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## Built-in LTL Model Checker

yes + proof

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## Counterexamples

possibly spurious

verified by Attestor

# Output: Specification Satisfied?

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## Built-in LTL Model Checker

yes + proof

don't know

no + counterexample



## Counterexamples

possibly spurious

verified by Attestor

generate non-spurious  
initial states

# Experiments

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75 benchmarks in total, 7 different data structures



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**Properties:** memory safety, shape, reachability, full traversal, output = input

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**Properties:** memory safety, shape, reachability, full traversal, output = input

**Lindstrom tree traversal** (no recursion, no stack)

memory safety	229 states	0.119 seconds
output = input	67941 states	8.901 seconds

**Recursive tree traversal**

memory safety	91 states	0.075 seconds
output = input	21738 states	1.714 seconds

**SLL to AVL tree**

balanced tree	7166 states	2.728 seconds
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# Conclusion

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~20k LOC (Java)



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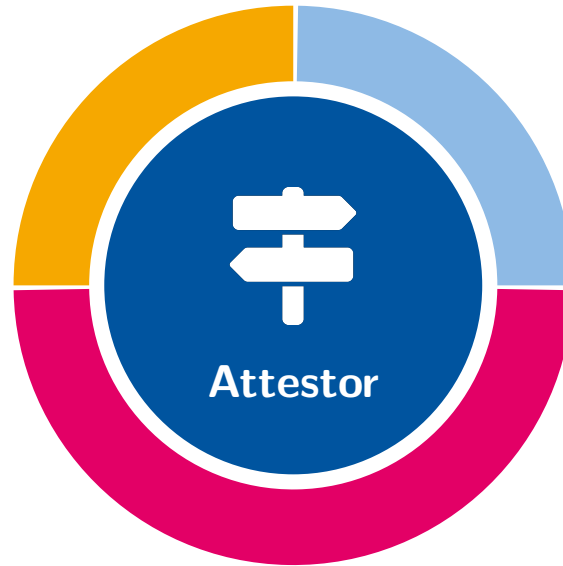


build with  
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~20k LOC (Java)



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<https://moves-rwth.github.io/attestor>